

**40V N+N-Channel Enhancement Mode MOSFET**
**Description**

The AP6H04LI uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

**General Features**

$V_{DS}=40V$   $I_D=6.3A$

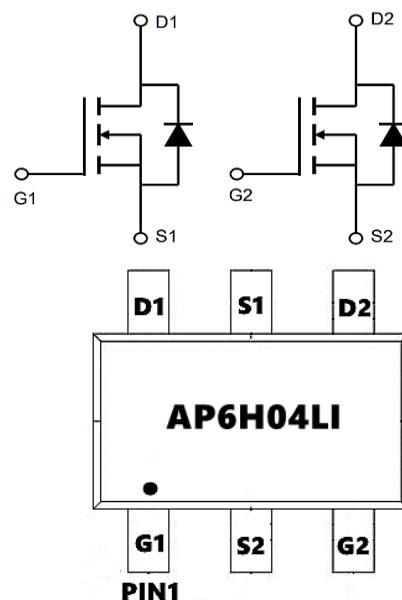
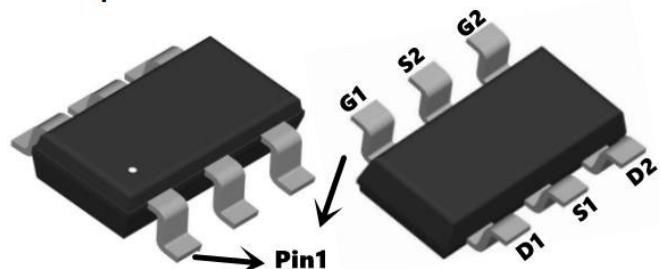
$R_{DS(ON)} < 38m\Omega$  @  $V_{GS}=10V$  (**Type: 28m $\Omega$** )

**Application**

Wireless charging

Boost driver

Brushless motor


**Top View**
**Bottom View**

**Package Marking and Ordering Information**

Product ID	Pack	Marking	Qty(PCS)
AP6H04LI	SOT23-6L	AP6H04LI	3000

**Absolute Maximum Ratings ( $T_c=25^\circ C$  unless otherwise noted)**

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	40	V
V <sub>GS</sub>	Gate-Source Voltage	$\pm 20$	V
I <sub>D</sub> @T <sub>A</sub> =25°C	Continuous Drain Current <sup>1</sup>	6.3	A
I <sub>D</sub> @T <sub>A</sub> =70°C	Continuous Drain Current <sup>1</sup>	4.2	A
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	30	A
E <sub>AS</sub>	Single Pulse Avalanche Energy <sup>3</sup>	31	mJ
P <sub>D</sub> @T <sub>A</sub> =25°C	Total Power Dissipation <sup>4</sup>	1.9	W
T <sub>TSG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C
R <sub>θJA</sub>	Thermal Resistance Junction-ambient <sup>1</sup> (t≤10s)	125	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-ambient <sup>1</sup>	60	°C/W

**40V N+N-Channel Enhancement Mode MOSFET**
**N-Channel Electrical Characteristics ( $T_J=25^{\circ}\text{C}$ , unless otherwise noted)**

Symbol	Parameter	Conditions	Min	Typ	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	40	---	---	V
$\Delta BVDSS/\Delta T_J$	BVDSS Temperature Coefficient	Reference to $25^{\circ}\text{C}$ , $I_D=1\text{mA}$	---	0.032	---	$\text{V}/^{\circ}\text{C}$
RDS(ON)	Static Drain-Source On-Resistance <sup>2</sup>	$V_{GS}=10\text{V}$ , $I_D=4\text{A}$	---	28	38	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$ , $I_D=3\text{A}$	---	40	50	
VGS(th)	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250\mu\text{A}$	1.0	1.5	2.5	V
$\Delta V_{GS(\text{th})}$	$V_{GS(\text{th})}$ Temperature Coefficient		---	-4.5	---	$\text{mV}/^{\circ}\text{C}$
IDSS	Drain-Source Leakage Current	$V_{DS}=32\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=25^{\circ}\text{C}$	---	---	1	$\text{uA}$
		$V_{DS}=32\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=55^{\circ}\text{C}$	---	---	5	
IGSS	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$ , $V_{DS}=0\text{V}$	---	---	$\pm 100$	nA
gfs	Forward Transconductance	$V_{DS}=5\text{V}$ , $I_D=4\text{A}$	---	8	---	S
R <sub>g</sub>	Gate Resistance	$V_{DS}=0\text{V}$ , $V_{GS}=0\text{V}$ , $f=1\text{MHz}$	---	2.4	4.8	$\Omega$
Q <sub>g</sub>	Total Gate Charge (4.5V)		---	5	---	nC
Qgs	Gate-Source Charge		---	1.54	---	
Qgd	Gate-Drain Charge		---	1.84	---	
Td(on)	Turn-On Delay Time	$V_{DD}=15\text{V}$ , $V_{GS}=10\text{V}$ , $R_G=3.3\text{k}\Omega$ $I_D=1\text{A}$	---	7.8	---	ns
T <sub>r</sub>	Rise Time		---	2.1	---	
Td(off)	Turn-Off Delay Time		---	29	---	
T <sub>f</sub>	Fall Time		---	2.1	---	
C <sub>iss</sub>	Input Capacitance	$V_{DS}=15\text{V}$ , $V_{GS}=0\text{V}$ , $f=1\text{MHz}$	---	452	---	pF
C <sub>oss</sub>	Output Capacitance		---	51	---	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	38	---	
IS	Continuous Source Current <sup>1,4</sup>	$V_G=V_D=0\text{V}$ , Force Current	---	---	4.5	A
ISM	Pulsed Source Current <sup>2,4</sup>		---	---	14	A
VSD	Diode Forward Voltage <sup>2</sup>	$V_{GS}=0\text{V}$ , $I_S=1\text{A}$ , $T_J=25^{\circ}\text{C}$	---	---	1.2	V

**Note :**

- 1、The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2、The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$
- 3、The power dissipation is limited by  $150^{\circ}\text{C}$  junction temperature
- 4、The data is theoretically the same as  $I_D$  and  $I_{DM}$  , in real applications , should be limited by total power dissipation.

## Typical Characteristics

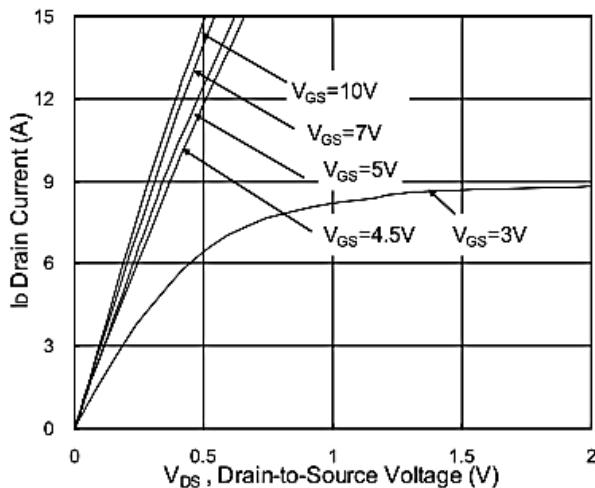


Fig.1 Typical Output Characteristics

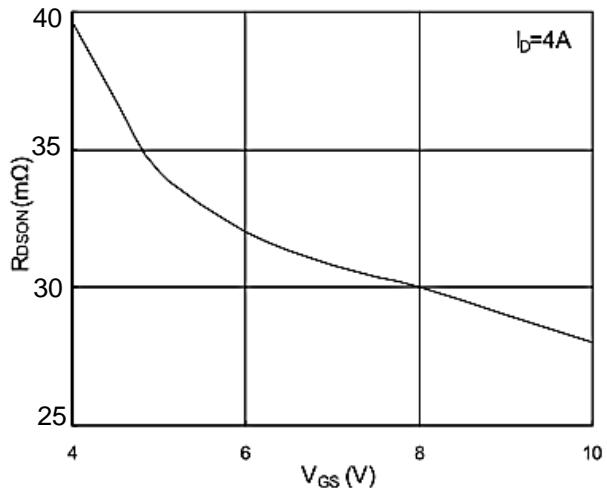


Fig.2 On-Resistance vs. Gate-Source

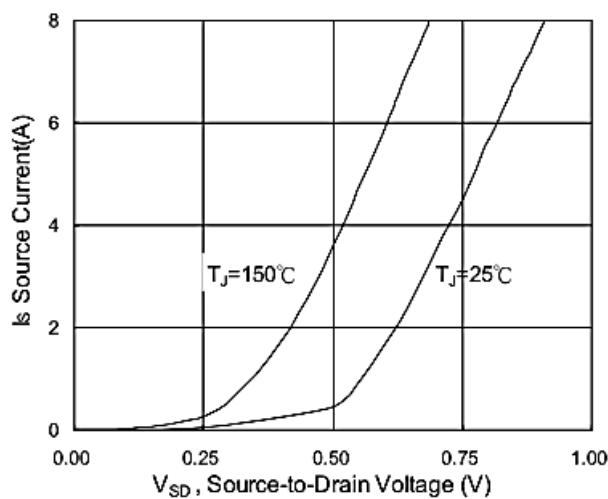


Fig.3 Forward Characteristics Of Reverse

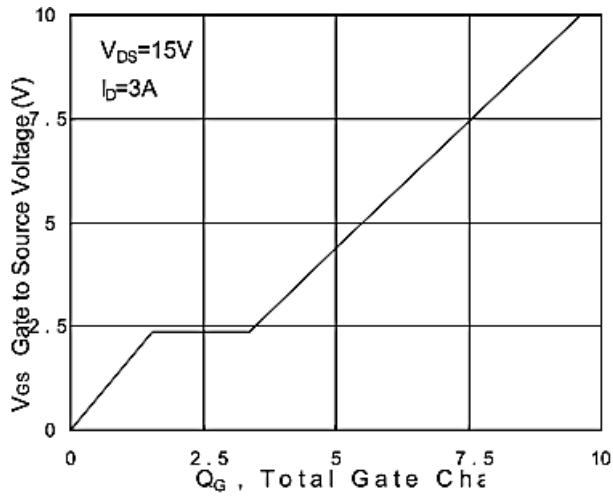


Fig.4 Gate-Charge Characteristics

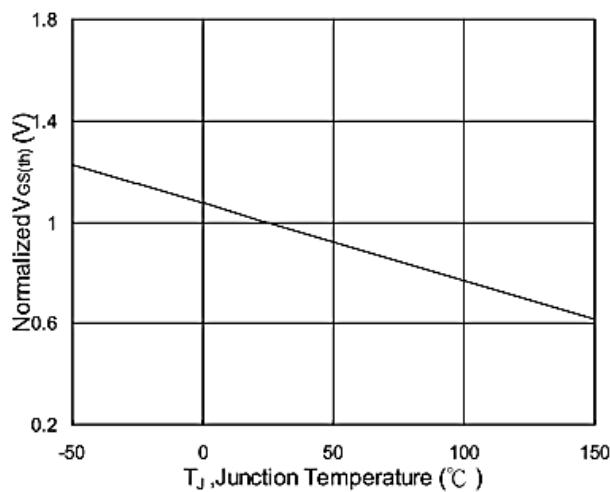


Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$

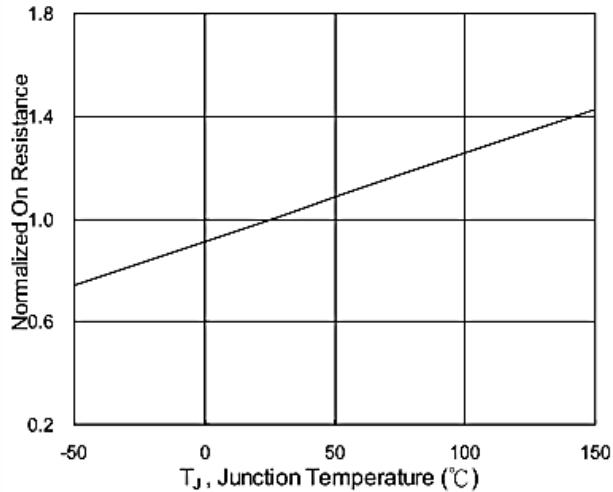
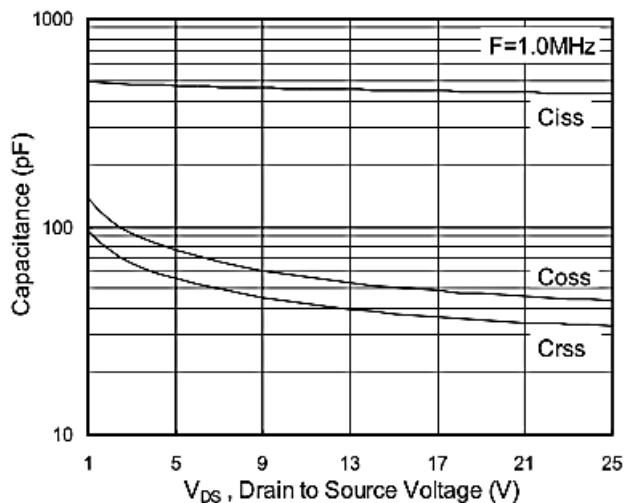
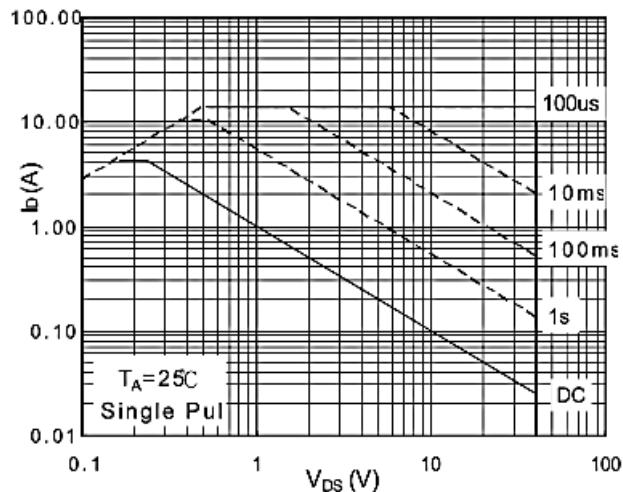
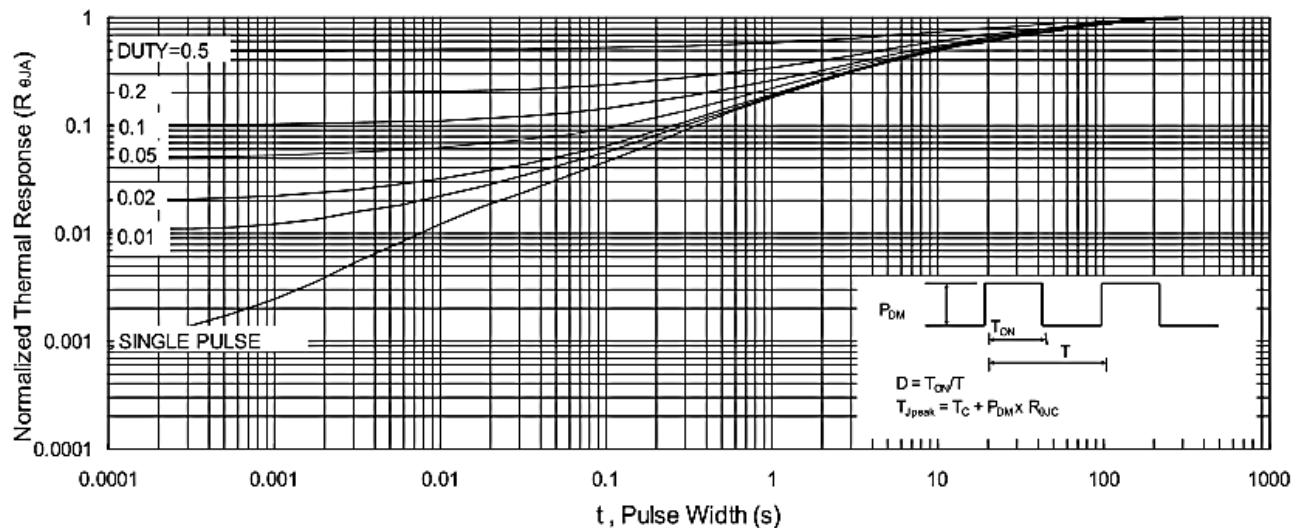
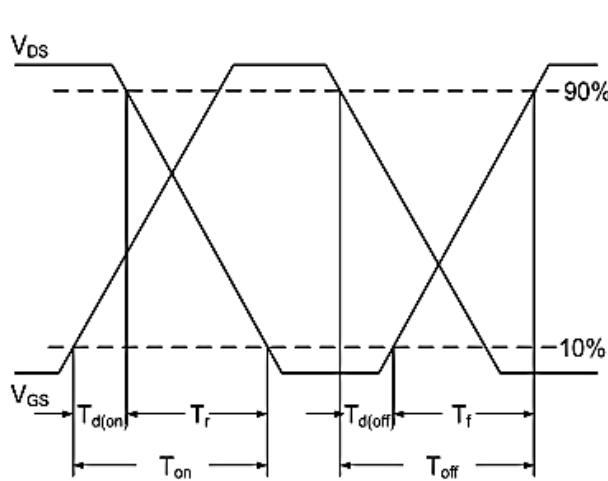
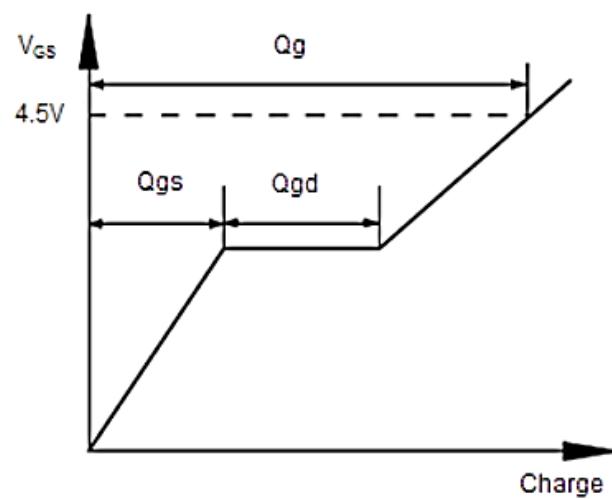
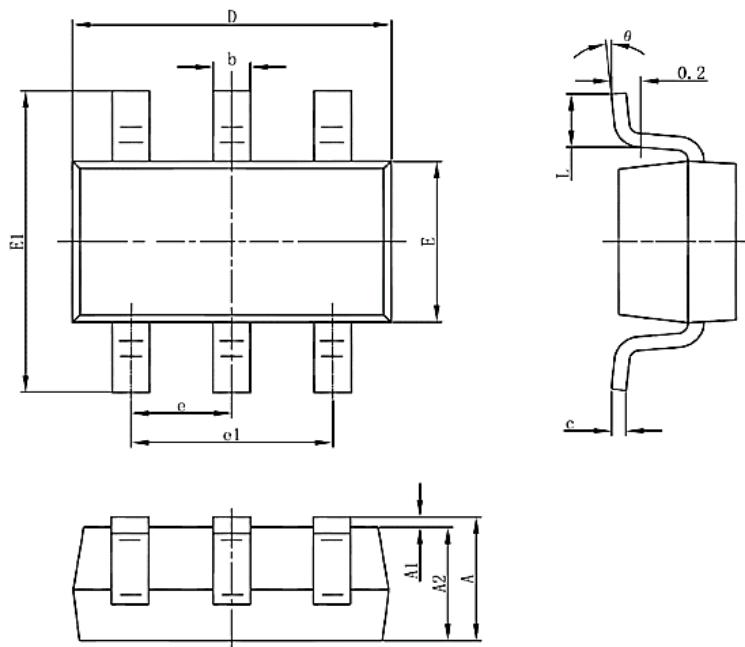


Fig.6 Normalized  $R_{DS(on)}$  vs.  $T_J$

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**Fig.7 Capacitance**

**Fig.8 Safe Operating Area**

**Fig.9 Normalized Maximum Transient Thermal Impedance**

**Fig.10 Switching Time Waveform**

**Fig.11 Gate Charge Waveform**

## Package Mechanical Data-SOT23-6-Double



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
C	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950 (BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0	8	0	8