

200V N-Channel Enhancement Mode MOSFET

Description

The AP40N20P/T is silicon N-channel Enhanced VDMOSFETs, is obtained by the self-aligned planar Technology which reduce the conduction loss, improve switching performance and enhance the avalanche energy. The transistor can be used in various power switching circuit for system miniaturization and higher efficiency.

General Features

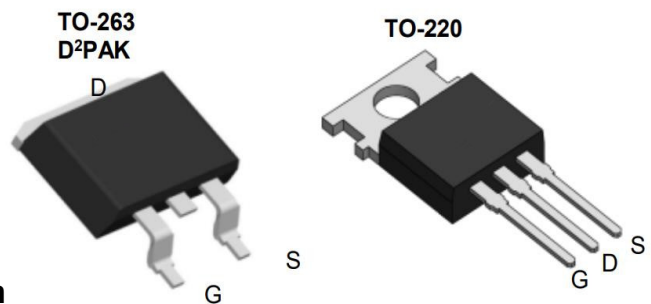
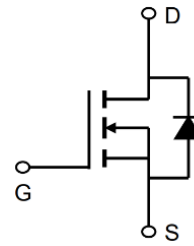
$V_{DS} = 200V$ $I_D = 40A$

$R_{DS(ON)} < 65m\Omega$ @ $V_{GS}=10V$ (Type: 50m Ω)

Application

Uninterruptible Power Supply(UPS)

Power Factor Correction (PFC)



Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AP40N20T	TO-263-3L	AP40N20T XXX YYYY	800
AP40N20P	TO-220-3L	AP40N20P XXX YYYY	1000

Absolute Maximum Ratings ($T_c=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Value	Unit
V_{DSS}	Drain-Source Voltage	200	V
I_D	Drain Current -continuous	40	A
I_{DM}	Drain Current -pulse	112	A
V_{GSS}	Gate-Source Voltage	± 30	V
E_{AS}	Single Pulsed Avalanche Energy	588	mJ
I_{AR}	Avalanche Current	28	A
E_{AR}	Repetitive Avalanche Current	15.8	mJ
dv/dt	Peak Diode Recovery dv/dt	5.5	V/ns
P_D $T_C=25^\circ C$	Power Dissipation	158	W
T_J, T_{STG}	Operating and Storage Temperature Range	$-55 \sim +150$	$^\circ C$
T_L	Maximum Lead Temperature for Soldering Purposes	300	$^\circ C$
$R_{th(j-c)}$	Thermal Resistance, Junction to Case	0.79	$^\circ C/W$
$R_{th(j-A)}$	Thermal Resistance, Junction to Ambient	62.5	$^\circ C/W$



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Electrical Characteristics (T_J=25°C, unless otherwise noted)

Symbol	Parameter	Tests conditions	Min	Typ	Max	Units
BV _{DSS}	Drain-Source Voltage	I _D =250μA, V _{GS} =0V	200	-	-	V
IDSS	Zero Gate Voltage Drain Current	V _{DS} =200V, V _{GS} =0V, T _C =25°C	-	-	1	μA
IGSSF	Gate-body leakage current, forward	V _{DS} =0V, V _{GS} =30V	-	-	100	nA
IGSSR	Gate-body leakage current, reverse	V _{DS} =0V, V _{GS} =-30V	-	-	-100	nA
VGS(th)	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D =250μA	2.0	3.0	4.0	V
RDS(ON)	Static Drain-Source On-Resistance	V _{GS} =10V, I _D =14.0A	-	50	65	mΩ
g _{fs}	Forward Transconductance	V _{DS} = 40V, I _D =14.0A	-	24	-	S
C _{iss}	Input capacitance	V _{DS} =25V, V _{GS} =0V, f=1.0MHz	-	2879	3742	pF
C _{oss}	Output capacitance		-	362	470	pF
C _{rss}	Reverse transfer capacitance		-	81	105	pF
t _{d(on)}	Turn-On delay time	V _{DD} =100V, I _D =28A, R _G =25Ω V _{GS} =10V (note 4, 5)	-	28	69	ns
t _r	Turn-On rise time		-	251	494	ns
t _{d(off)}	Turn-Off delay time		-	309	617	ns
t _f	Turn-Off Fall time		-	220	412	ns
Q _g	Total Gate Charge	V _{DS} =160V, I _D =28A V _{GS} =10V	-	103	136	nC
Q _{gs}	Gate-Source charge		-	16	-	nC
Q _{gd}	Gate-Drain charge		-	53	-	nC
IS	Continuous Body Diode Current	T _C = 25 °C	-	-	28	A
ISM	Pulsed Diode Forward Current		-	-	112	A
VSD	Body Diode Voltage	V _{GS} =0V, I _S =28A	-		1.4	V
t _{rr}	Reverse recovery time	V _{GS} =0V, I _S =28A dI _F /dt=100A/μs		218		ns
Q _{rr}	Reverse recovery charge			1.91		μC

Note :

- 1、 The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2、 The EAS data shows Max. rating . L=1.5mH, I_{AS}=28A, V_{DB}=50V, R_G=25 Ω, Starting T_J=25°C
- 3、 The test condition is Pulse Test: Pulse width ≤ 300μs, Duty Cycle ≤ 1%
- 4、 The power dissipation is limited by 150°C junction temperature
- 5、 The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.

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Electrical Characteristics

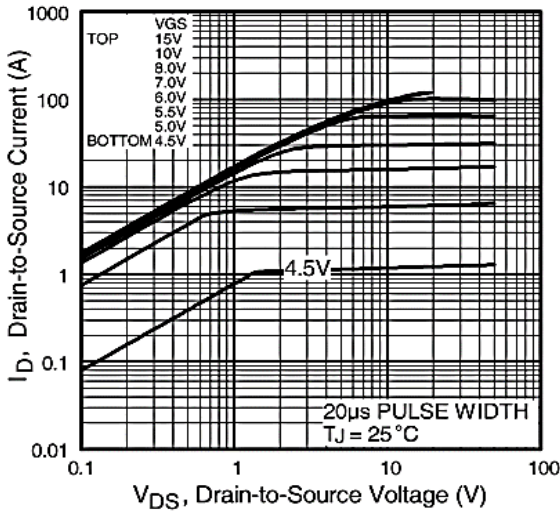


Fig 1. Typical Output Characteristics

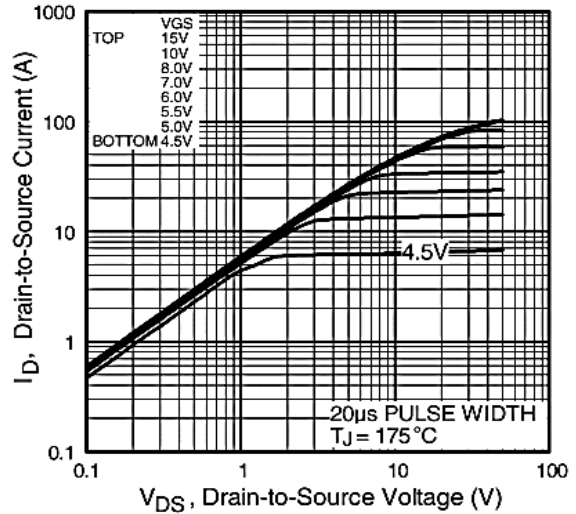


Fig 2. Typical Output Characteristics

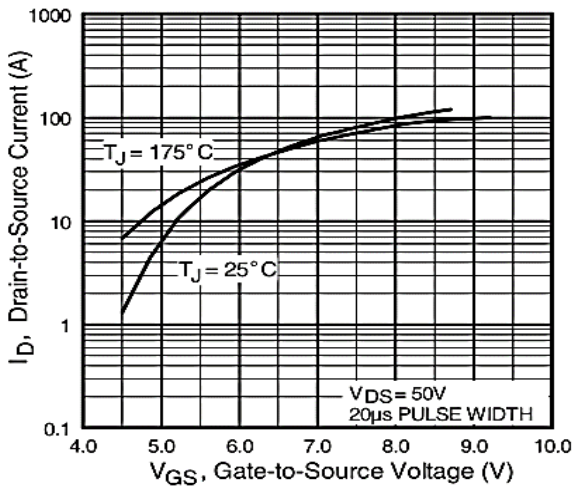


Fig 3. Typical Transfer Characteristics

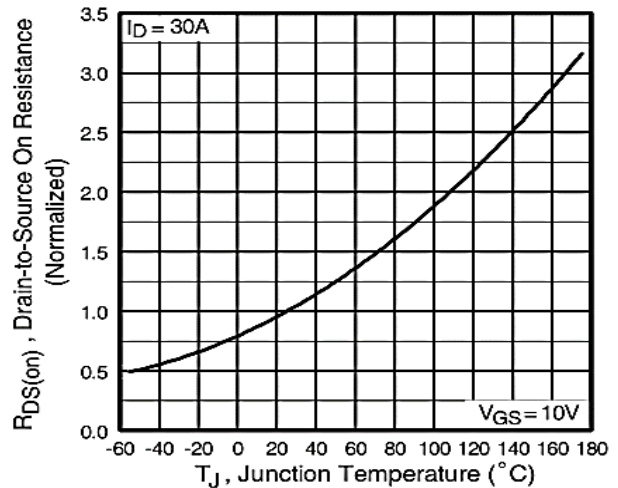


Fig 4. Normalized On-Resistance Vs. Temperature

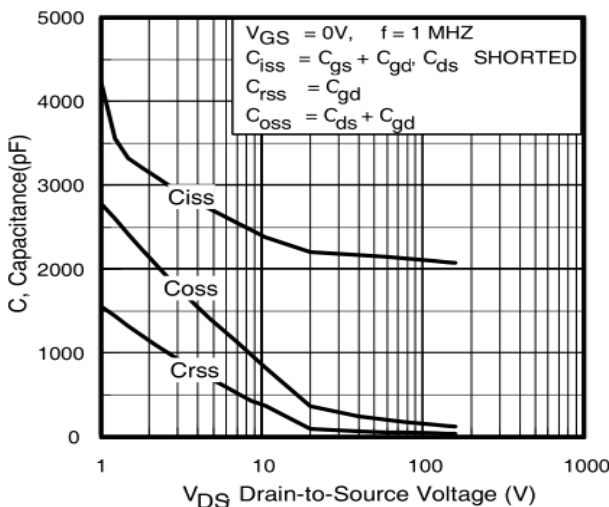


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

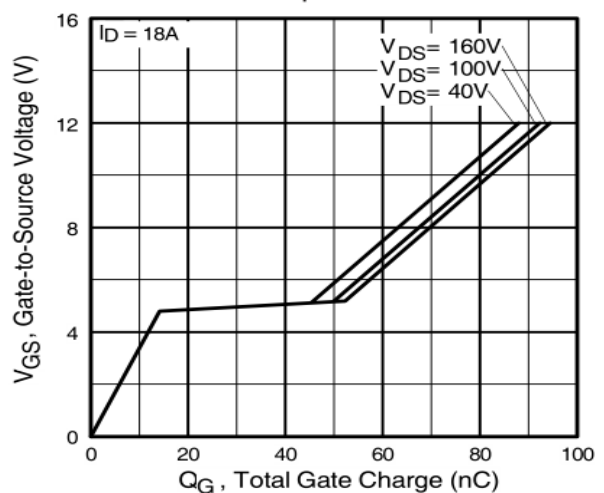


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

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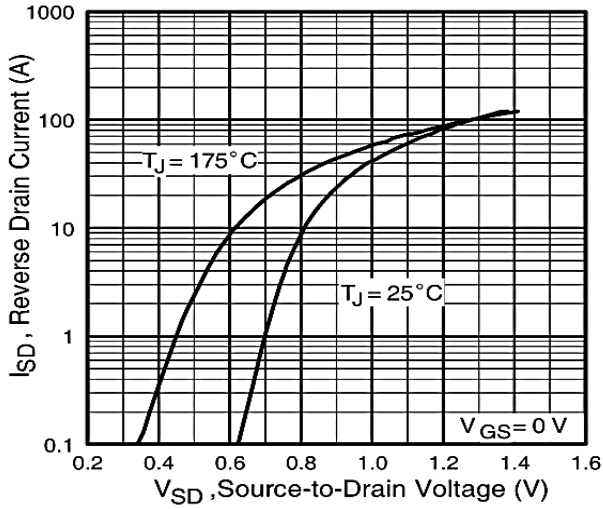


Fig 7. Typical Source-Drain Diode Forward Voltage

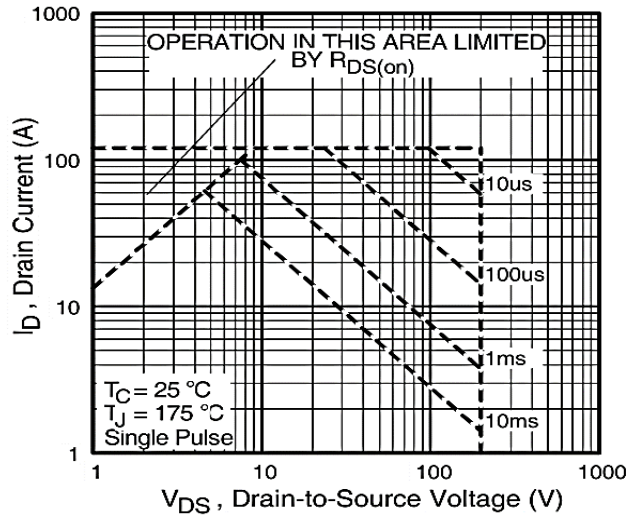


Fig 8. Maximum Safe Operating Area

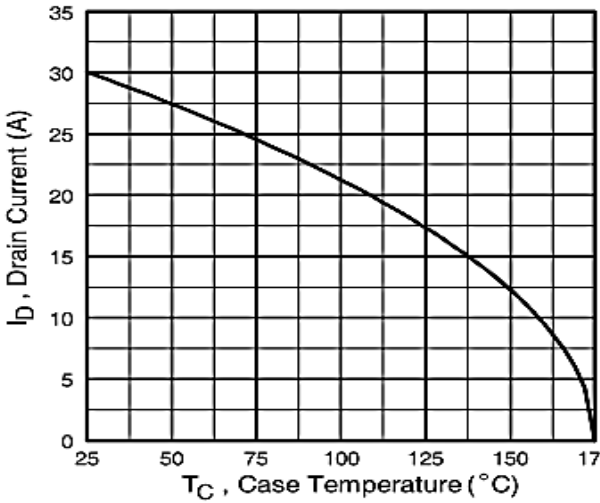


Fig 9. Maximum Drain Current Vs. Case Temperature

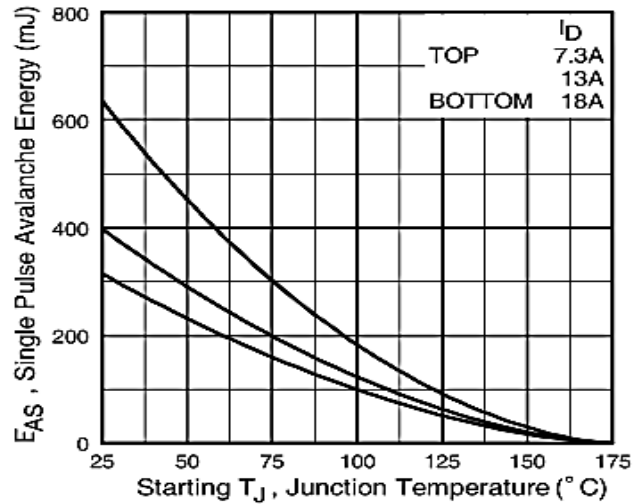


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

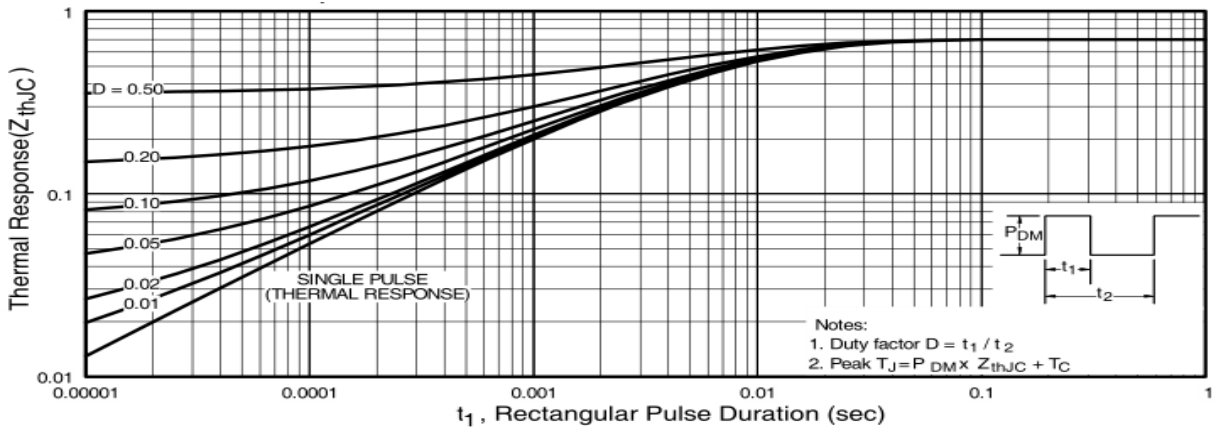
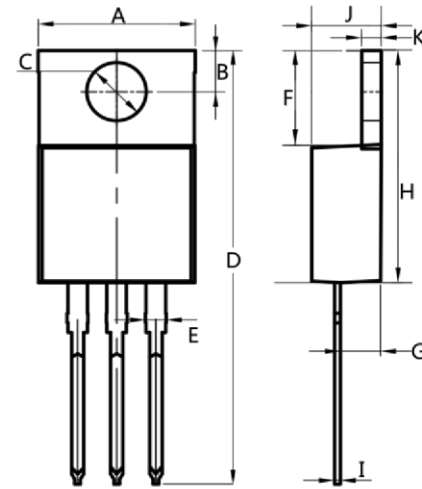


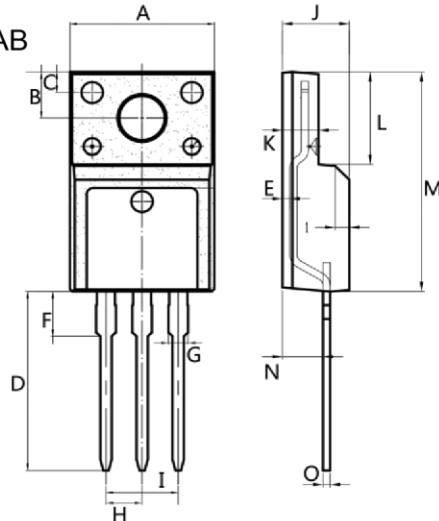
Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

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TO-220AB


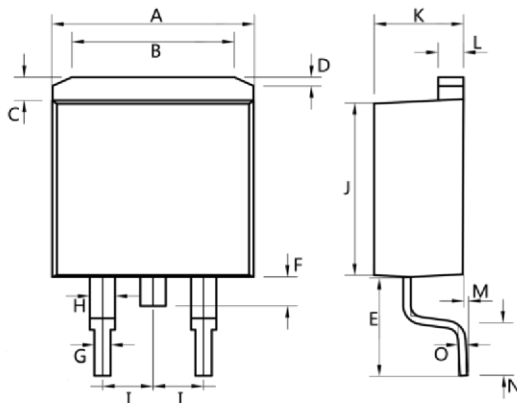
Dim.	Min.	Max.
A	10.0	10.4
B	2.5	3.0
C	3.5	4.0
D	28.0	30.0
E	1.1	1.5
F	6.2	6.6
G	2.9	3.3
H	15.0	16.0
I	0.35	0.45
J	4.3	4.7
K	1.2	1.4

All Dimensions in millimeter

ITO-220AB


Dim.	Min.	Max.
A	9.9	10.3
B	2.9	3.5
C	1.15	1.45
D	12.75	13.25
E	0.55	0.75
F	3.1	3.5
G	1.25	1.45
H	Typ 2.54	
I	Typ 5.08	
J	4.55	4.75
K	2.4	2.7
L	6.35	6.75
M	15.0	16.0
N	2.75	3.15
O	0.45	0.60

All Dimensions in millimeter

TO-263


Dim.	Min.	Max.
A	10.0	10.5
B	7.25	7.75
C	1.3	1.5
D	0.55	0.75
E	5.0	6.0
F	1.4	1.6
G	0.75	0.95
H	1.15	1.35
I	Typ 2.54	
J	8.4	8.6
K	4.4	4.6
L	1.25	1.45
M	0.02	0.1
N	2.4	2.8
O	0.35	0.45

All Dimensions in millimeter