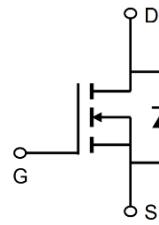


100V N-Channel Enhancement Mode MOSFET

Description

The AP70N10F/P/T uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.



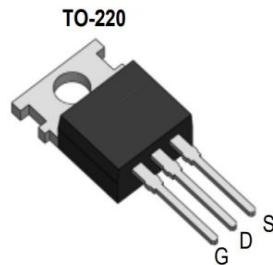
General Features

$V_{DS} = 100V$ $I_D = 70A$

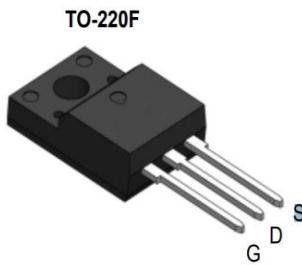
$R_{DS(ON)} < 22m\Omega$ @ $V_{GS}=10V$ (Type: 16m Ω)

Application

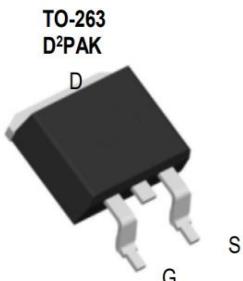
Automotive lighting



Load switch



Uninterruptible power supply



Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AP70N10F	TO-220F-3L	AP70N10F XXX YYYY	1000
AP70N10P	TO-220-3L	AP70N10P XXX YYYY	1000
AP70N10T	TO-263-3L	AP70N10T XXX YYYY	800

Absolute Maximum Ratings (TC=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D @ $T_c=25^\circ C$	Drain Current, V_{GS} @ 10V	70	A
I_D @ $T_c=100^\circ C$	Drain Current, V_{GS} @ 10V	56	A
IDM	Pulsed Drain Current ¹	210	A
P_D @ $T_c=25^\circ C$	Total Power Dissipation	90	W
TSTG	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C
$R_{\theta JA}$	Maximum Thermal Resistance, Junctionambient	62.5	°C/W
$R_{\theta JC}$	Maximum Thermal Resistance, Junction-case	1.4	°C/W

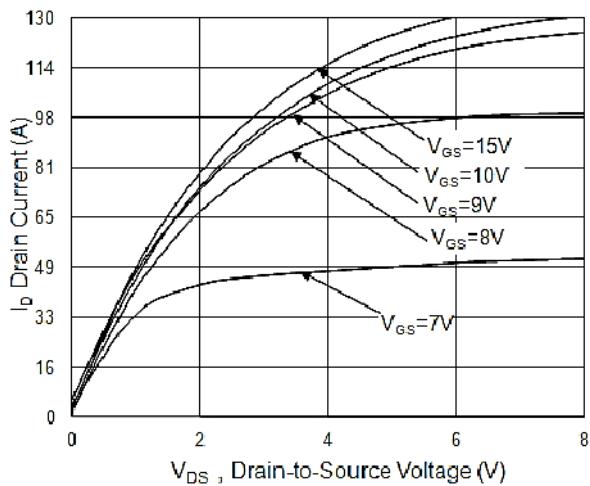
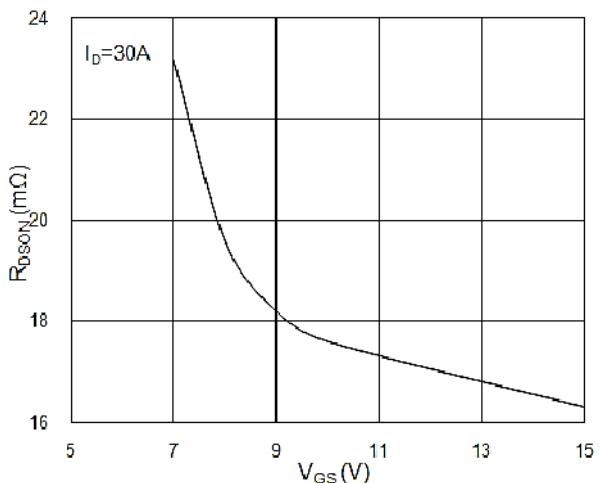
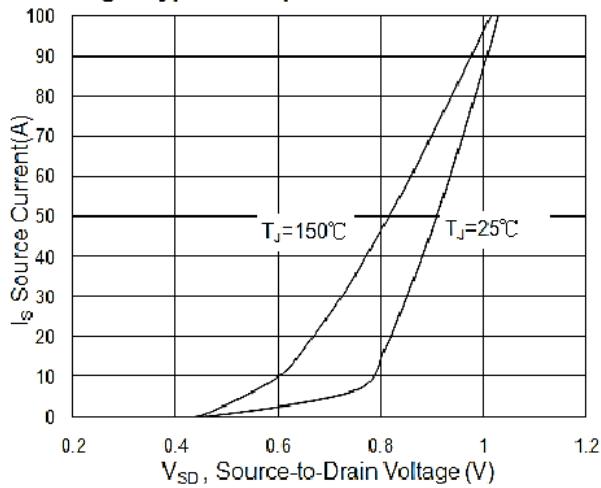
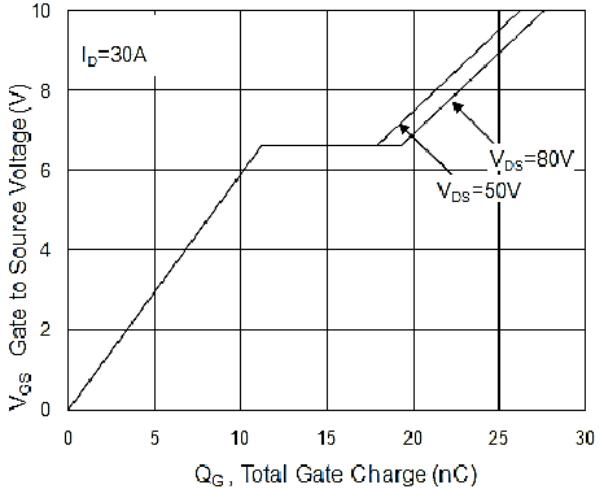
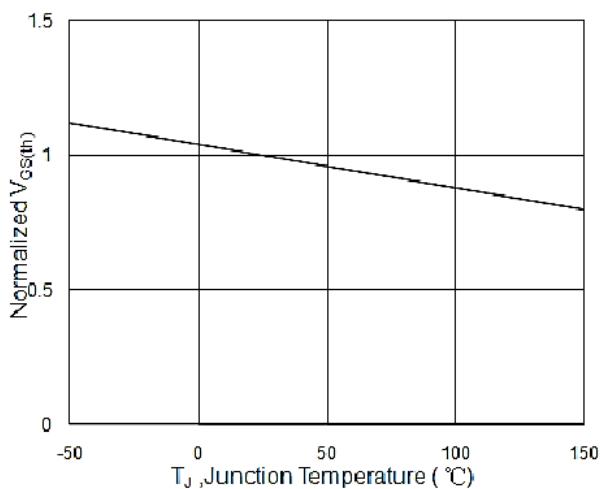
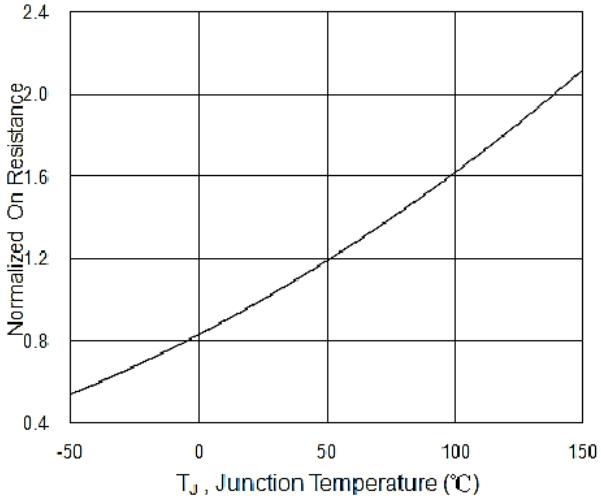


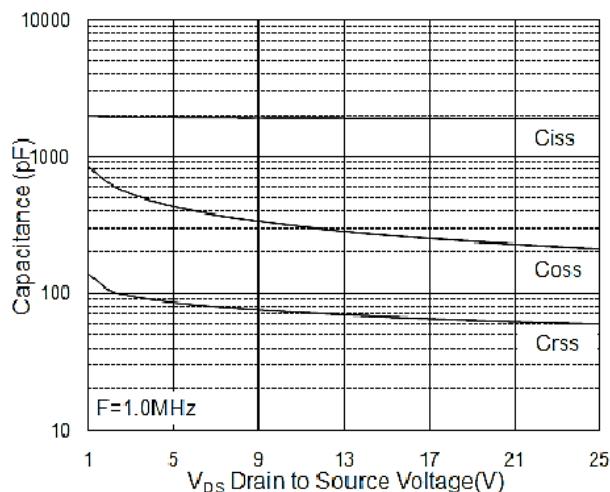
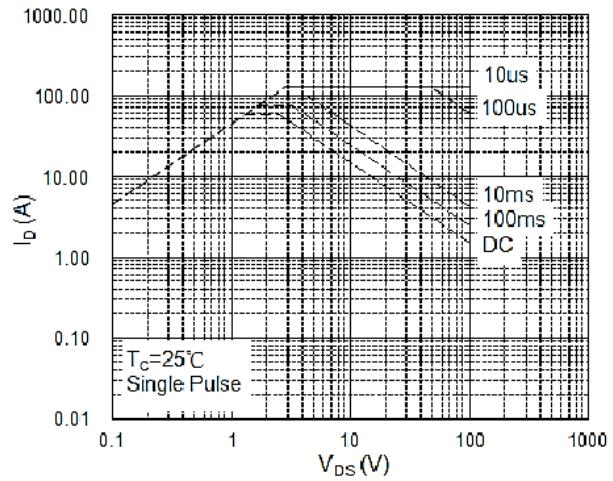
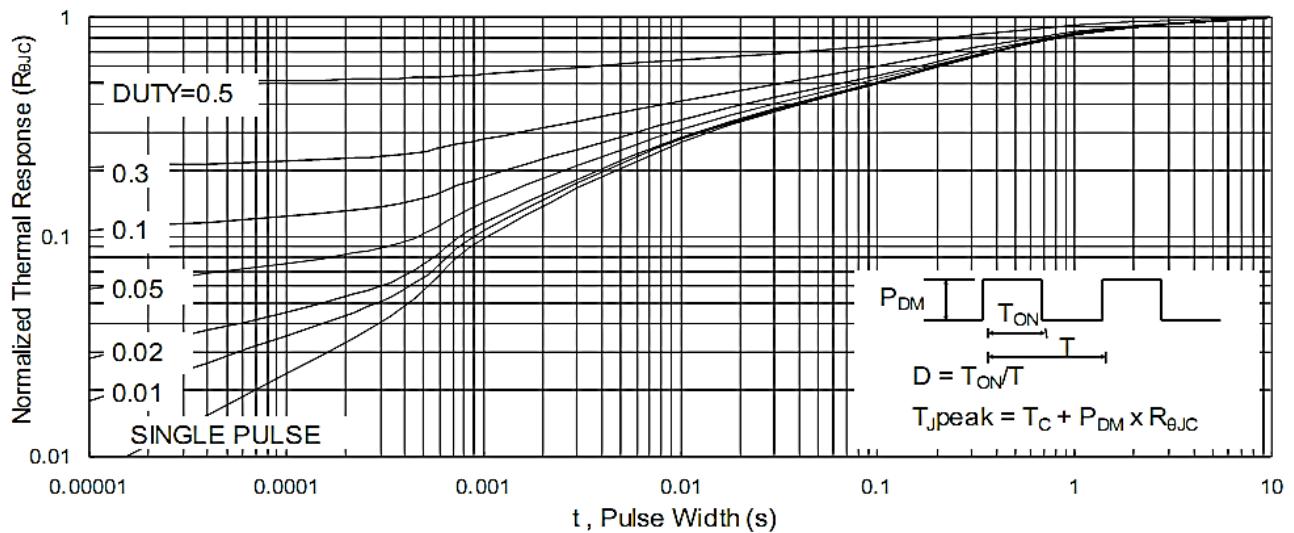
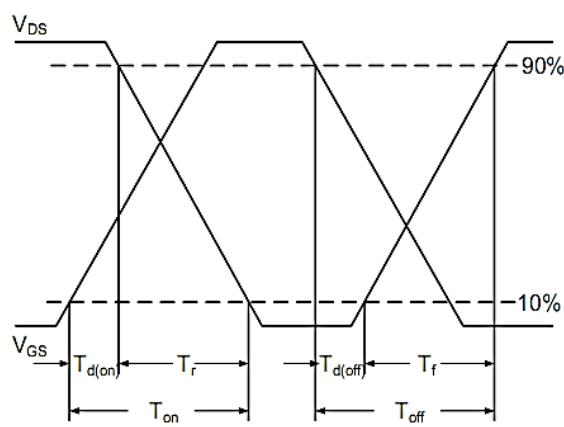
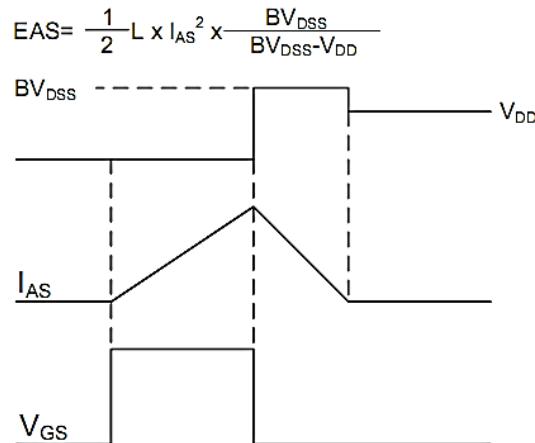
100V N-Channel Enhancement Mode MOSFET
Electrical Characteristics@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	100	110	---	V
RDS(ON)	Static Drain-Source On-Resistance ²	V _{GS} =10V , I _D =30A	---	16	22	mΩ
VGS(th)	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	2.0	3.0	4.0	V
IDSS	Drain-Source Leakage Current	V _{DS} =80V , V _{GS} =0V , T _J =25°C	---	---	1	uA
		V _{DS} =80V , V _{GS} =0V , T _J =55°C	---	---	5	
IGSS	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V	---	---	±100	nA
gfs	Forward Transconductance	V _{DS} =5V , I _D =30A	---	31	---	S
R _g	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz	---	1.9	3.8	Ω
Q _g	Total Gate Charge (10V)	V _{DS} =80V , V _{GS} =10V , I _D =30A	---	27.6	---	nC
Qgs	Gate-Source Charge		---	11.4	---	
Qgd	Gate-Drain Charge		---	7.9	---	
Td(on)	Turn-On Delay Time	V _{DD} =50V , V _{GS} =10V , R _G =3.3Ω, I _D =30A	---	16.5	---	ns
T _r	Rise Time		---	35	---	
Td(off)	Turn-Off Delay Time		---	17.5	---	
T _f	Fall Time		---	12	---	
Ciss	Input Capacitance	V _{DS} =15V , V _{GS} =0V , f=1MHz	---	1890	---	pF
Coss	Output Capacitance		---	268	---	
Crss	Reverse Transfer Capacitance		---	67	---	
IS	Continuous Source Current ^{1,5}	V _G =V _D =0V , Force Current	---	---	58	A
ISM	Pulsed Source Current ^{2,5}		---	---	130	A
VSD	Diode Forward Voltage ²	V _{GS} =0V , I _s =1A , T _J =25°C	---	---	1.2	V
t _{rr}	Reverse Recovery Time	I _F =30A , dI/dt=100A/μs , T _J =25°C	---	22	---	nS
Q _{rr}	Reverse Recovery Charge		---	20	---	nC

Note :

- 1、The data tested by surface mounted on a 1 inch 2 FR-4 board with 2OZ copper.
- 2、The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%
- 3、The EAS data shows Max. rating . The test condition is VDD=72V,VGS=10V,L=0.1mH,IAS=40A
- 4、The power dissipation is limited by 150°C junction temperature
- 5、The data is theoretically the same as I D and I DM , in real applications , should be limited by total power dissipation.

100V N-Channel Enhancement Mode MOSFET
Typical Characteristics

Fig.1 Typical Output Characteristics

Fig.2 On-Resistance v.s Gate-Source

Fig.3 Forward Characteristics of Reverse

Fig.4 Gate-Charge Characteristics

Fig.5 Normalized $V_{GS(th)}$ vs. T_J

Fig.6 Normalized $R_{DS(on)}$ vs. T_J

100V N-Channel Enhancement Mode MOSFET

Fig.7 Capacitance

Fig.8 Safe Operating Area

Fig.9 Normalized Maximum Transient Thermal Impedance

Fig.10 Switching Time Waveform

Fig.11 Unclamped Inductive Switching Waveform

100V N-Channel Enhancement Mode MOSFET
