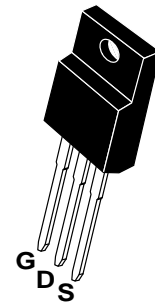


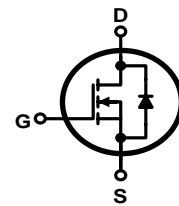


Features:

- Low Intrinsic Capacitances.
- Excellent Switching Characteristics.
- Extended Safe Operating Area.
- Unrivalled Gate Charge :Qg= 44nC (Typ.).
- BVDSS=700V, I<sub>D</sub>=12A
- R<sub>DS(on)</sub> : 0.75Ω (Max) @V<sub>G</sub>=10V
- 100% Avalanche Tested



Schematic diagram



Marking Diagram



- Y = Year
- A = Assembly Location
- WW = Work Week
- FIR12N70F = Specific Device Code

**Absolute Maximum Ratings\*** (T<sub>c</sub>=25°C Unless otherwise noted)

Symbol	PARAMETER	Value	Unit
V <sub>DSS</sub>	Drain-Source Voltage	700	V
I <sub>D</sub>	Drain Current	T <sub>C</sub> =25°C	12
		T <sub>C</sub> =100°C	7.9
V <sub>GS(TH)</sub>	Gate Threshold Voltage	±30	V
E <sub>AS</sub>	Single Pulse Avalanche Energy (note1)	850	mJ
I <sub>AR</sub>	Avalanche Current (note2)	12	A
P <sub>D</sub>	Power Dissipation (T <sub>c</sub> =25°C)	70	W
T <sub>j</sub>	Junction Temperature(MAX)	150	°C
T <sub>stg</sub>	Storage Temperature	-55~+150	°C
TL	Maximum lead temperature for soldering purpose, 1/8" from case for 5 seconds	300	°C

**Thermal Characteristics**

Symbol	PARAMETER	Typ.	MAX.	Unit
R <sub>θJC</sub>	Thermal Resistance, Junction to Case	-	2.27	°C/W
R <sub>θJA</sub>	Thermal Resistance, Junction to Ambient	-	62.5	°C/W



## Electrical Characteristics (Tc=25°C unless other wise noted)

Symbol	Parameter	Test Condition	MIN.	Typ.	MAX.	Unit
<b>Off Characteristics</b>						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	I <sub>D</sub> =250μA, V <sub>GS</sub> =0	700	-	-	V
ΔBVDSS/ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> =250μA, Reference to 25°C	-	0.71	-	V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =700V, V <sub>GS</sub> =0V	-	-	10	μA
		V <sub>DS</sub> =560V, Tc=125°C	-	-	100	
I <sub>GSSF</sub>	Gate-body leakage Current, Forward	V <sub>GS</sub> =+30V, V <sub>DS</sub> =0V	-	-	100	nA
I <sub>GSSR</sub>	Gate-body leakage Current, Reverse	V <sub>GS</sub> =-30V, V <sub>DS</sub> =0V	-	-	-100	nA
<b>On Characteristics</b>						
V <sub>GS(TH)</sub>	Gate Threshold Voltage	I <sub>D</sub> =250μA, V <sub>DS</sub> =V <sub>GS</sub>	2	-	4	V
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	I <sub>D</sub> =6A, V <sub>GS</sub> =10V	-	0.68	0.75	Ω
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =25V, V <sub>GS</sub> =0, f=1.0MHz	-	2300	-	pF
C <sub>oss</sub>	Output Capacitance		-	68	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	7.7	-	pF
<b>Switching Characteristics</b>						
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =350V, I <sub>D</sub> =12A R <sub>G</sub> =25Ω (Note 3,4)	-	70	-	ns
T <sub>r</sub>	Turn-On Rise Time		-	160	-	ns
T <sub>d(off)</sub>	Turn-Off Delay Time		-	300	-	ns
T <sub>f</sub>	Turn-Off Rise Time		-	160	-	ns
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =560, V <sub>GS</sub> =10V, I <sub>D</sub> =12A (Note 3,4)	-	44	48	nC
Q <sub>gs</sub>	Gate-Source Charge		-	7	-	nC
Q <sub>gd</sub>	Gate-Drain Charge		-	19	-	nC
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
I <sub>S</sub>	Max. Diode Forward Current	-	-	-	12	A
I <sub>SM</sub>	Max. Pulsed Forward Current	-	-	-	48	A
V <sub>SD</sub>	Diode Forward Voltage	I <sub>D</sub> =12A	-	-	1.5	V
T <sub>rr</sub>	Reverse Recovery Time	I <sub>S</sub> =12A, V <sub>GS</sub> =0V diF/dt=100A/μs (Note3)	-	380	-	nS
Q <sub>rr</sub>	Reverse Recovery Charge		-	3.4	-	μC

- Notes : 1, L=8.4mH, I<sub>AS</sub>=12A, V<sub>DD</sub>=50V, R<sub>G</sub>=25Ω, Starting T<sub>J</sub>=25°C  
 2, Repetitive Rating : Pulse width limited by maximum junction temperature  
 3, Pulse Test : Pulse Width ≤ 300μs, Duty Cycle ≤ 2%  
 4, Essentially Independent of Operating Temperature



# Typical Characteristics

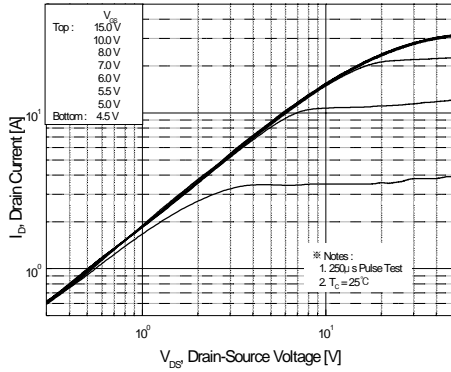


Figure 1. On-Region Characteristics

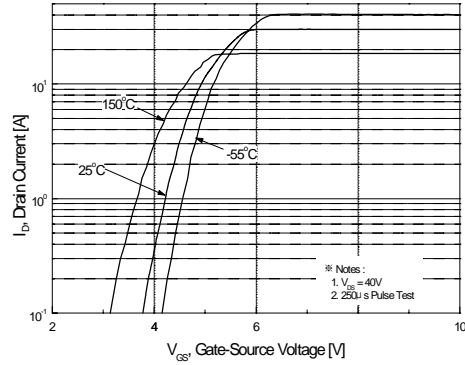


Figure 2. Transfer Characteristics

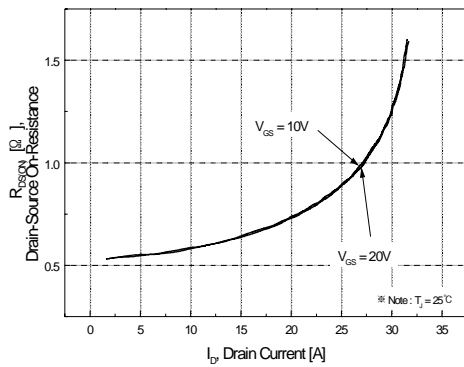


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

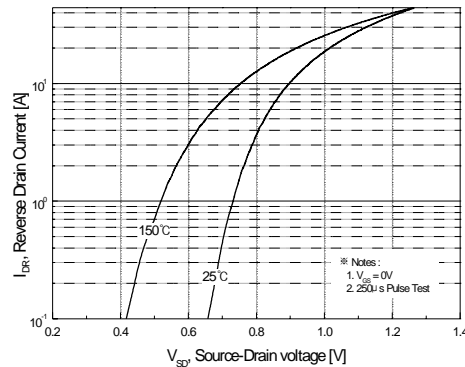


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

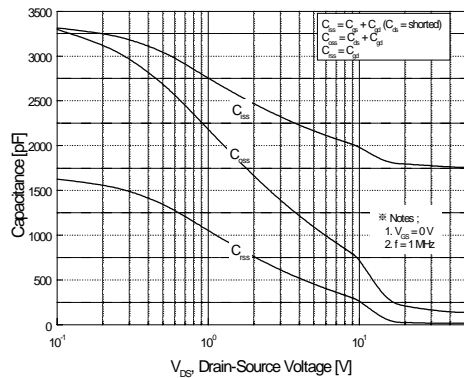


Figure 5. Capacitance Characteristics

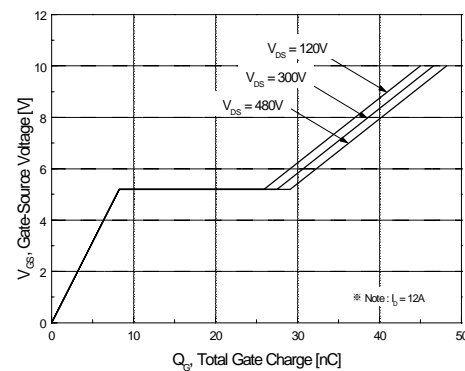


Figure 6. Gate Charge Characteristics

Typical Characteristics (Continued)

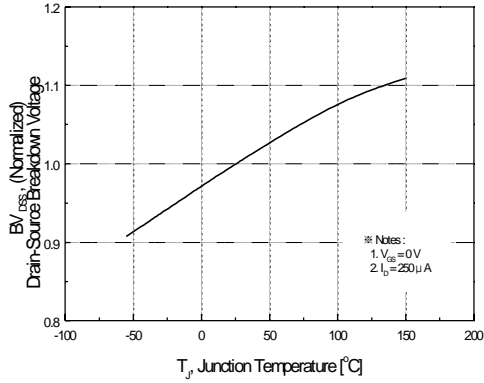


Figure 7. Breakdown Voltage Variation vs Temperature

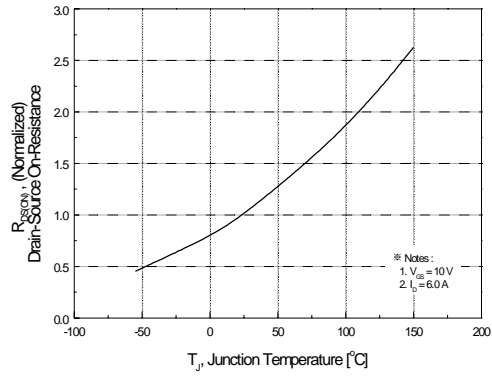


Figure 8. On-Resistance Variation vs Temperature

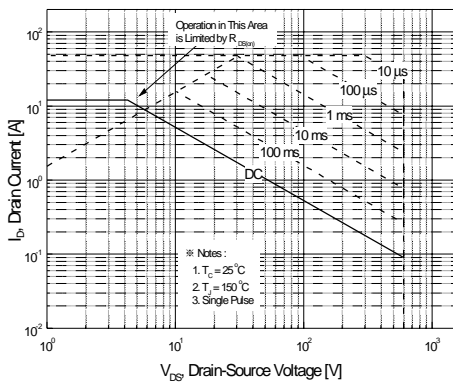


Figure 9-2. Maximum Safe Operating Area

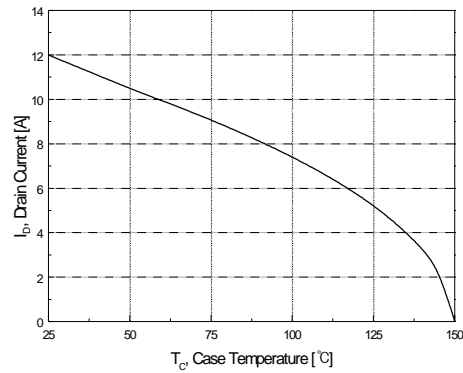


Figure 10. Maximum Drain Current vs Case Temperature

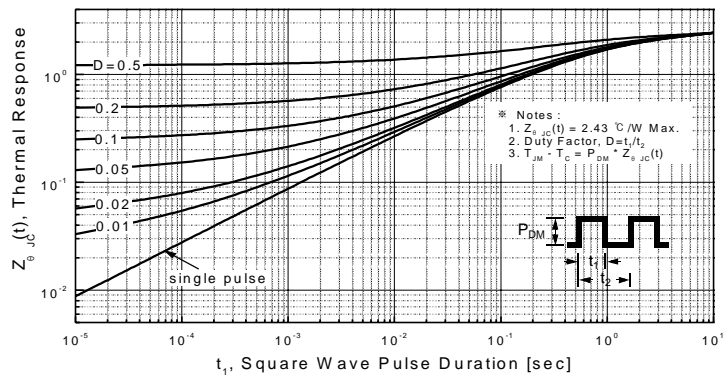
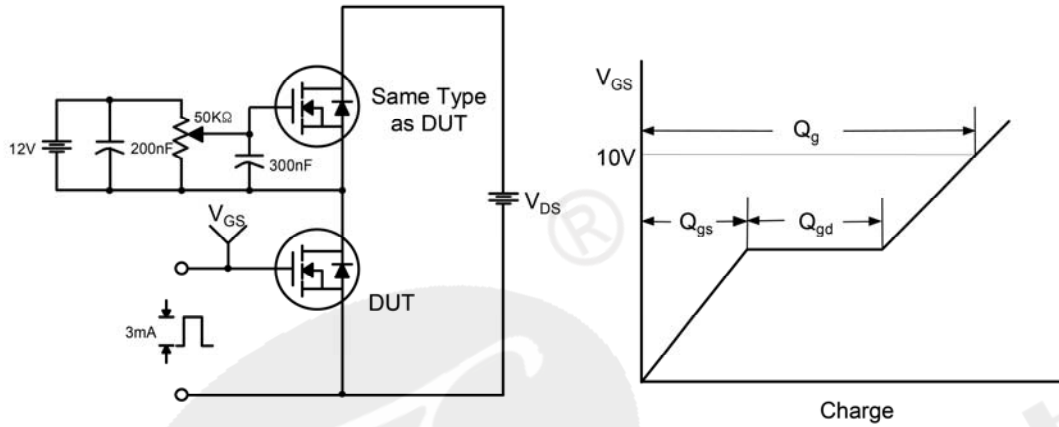
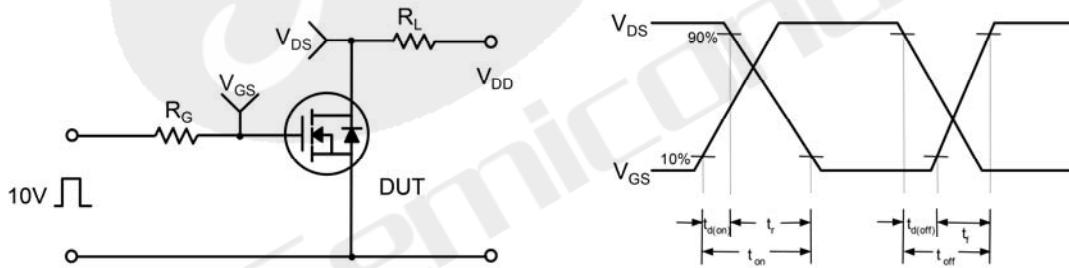


Figure 11-2. Transient Thermal Response Curve

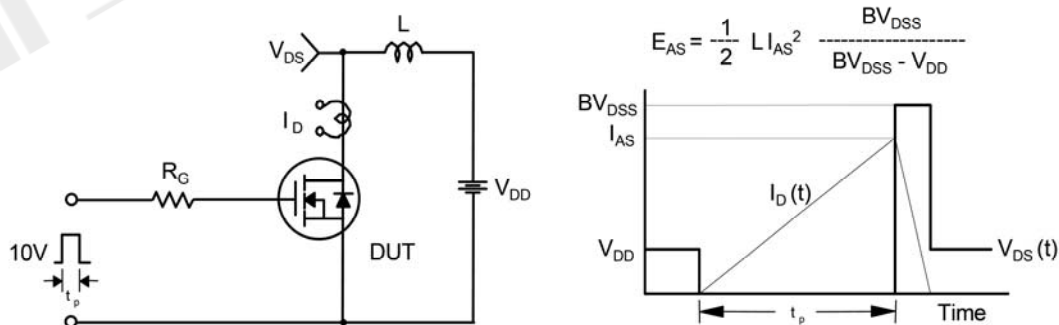
**Gate Charge Test Circuit & Waveform**



**Resistive Switching Test Circuit & Waveforms**

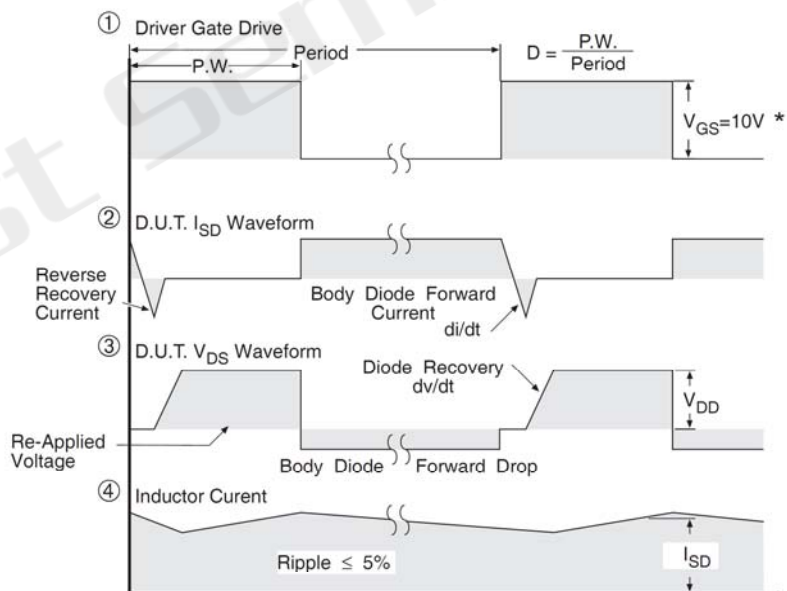
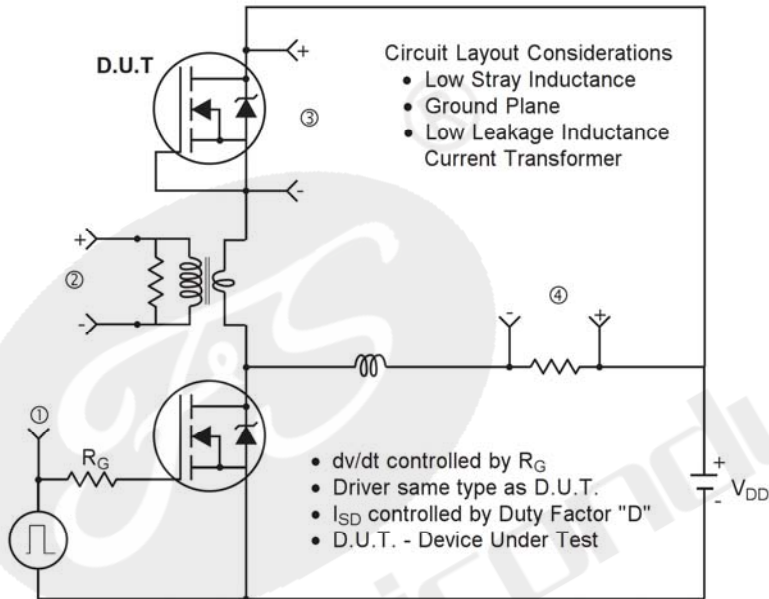


**Unclamped Inductive Switching Test Circuit & Waveforms**



Peak Diode Recovery dv/dt Test Circuit & Waveform

Peak Diode Recovery dv/dt Test Circuit



\*  $V_{GS} = 5V$  for Logic Level Devices





Declaration

- FIRST reserves the right to change the specifications, the same specifications of products due to different packaging line mold, the size of the appearance will be slightly different, shipped in kind, without notice! Customers should obtain the latest version information before ordering, and verify whether the relevant information is complete and up-to-date.
- Any semiconductor product under certain conditions has the possibility of failure or failure, The buyer has the responsibility to comply with safety standards and take safety measures when using FIRST products for system design and manufacturing, To avoid To avoid potential failure risks, which may cause personal injury or property damage!
- Product promotion endless, our company will wholeheartedly provide customers with better products!

**ATTACHMENT**

Revision History

Date	REV	Description	Page
2022 .01.01	1.0	Initial release	