ESDLC12VD3

Description

ESDLC12VD3 is designed to protect voltage sensitive components from ESD and transient voltage events. Excellent clamping capability, low leakage, and fast response time, make these parts ideal for ESD protection on designs where board space is at a premium. Because of its small size, ultra-low capacitance values, it is very suitable for signal port and board space speed transmission is very small places, such as Ethernet, mobile phones, MP3 players, digital cameras and other portable.

Features

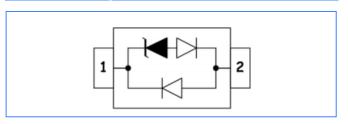
- Ultra low leakage: nA levelOperating voltage: 12V
- Package: SOD-323
- Protects one I/O line (unidirection)
- Low clamping voltage
- Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test

Air discharge: ±15kV Contact discharge: ±8kV

- IEC61000-4-4 (EFT) 40A (5/50ns)
- IEC61000-4-5 (Lightning) 12A (8/20µs)



Functional Diagram



Applications

- Cell Phone Handsets and Accessories
- Microprocessor based equipment
- Personal Digital Assistants (PDA's)
- Notebooks, Desktops, and Servers
- Portable Instrumentation
- Peripherals
- USB Interface

Absolute Maximum Ratings(Tamb=25 °C unless otherwise specified)

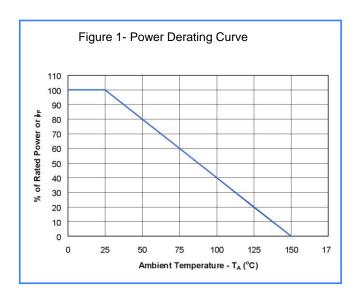
Parameter	Symbol	Value	Unit	
Peak Pulse Power (8/20µs)	P _{PP}	Watts		
ESD per IEC 61000-4-2 (Air)	V	±15	KV	
ESD per IEC 61000-4-2 (Contact)	V_{ESD}	±8	KV	
Lead Soldering Temperature	TL	260 (10 sec)	°C	
Operating Temperature Range	TJ	-55 to +150	°C	
Storage Temperature Range	T _{STJ}	-55 to +150	°C	

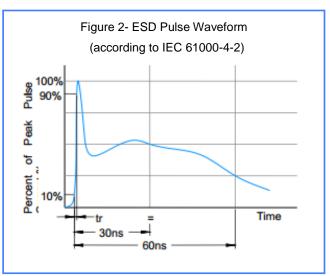


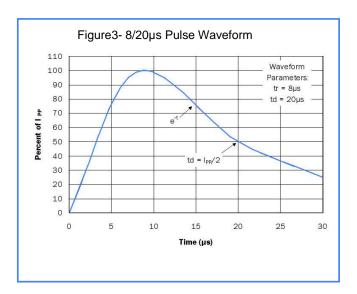
Electrical Characteristics (TA = 25 $^{\circ}$ C unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Reverse Stand-off Voltage	VRWM				12	V
Reverse Breakdown Voltage	VBR	It = 1mA	13.3			V
Reverse Leakage Current	IR	VR =VRWM			1	μΑ
Clamping Voltage	VC	IPP=1A, tP = 8/20µs			19	V
Clamping Voltage	VC	IPP=6A, tP = 8/20µs			28.6	V
Junction Capacitance	CJ	VR=0V, f = 1MHz			0.4	pF

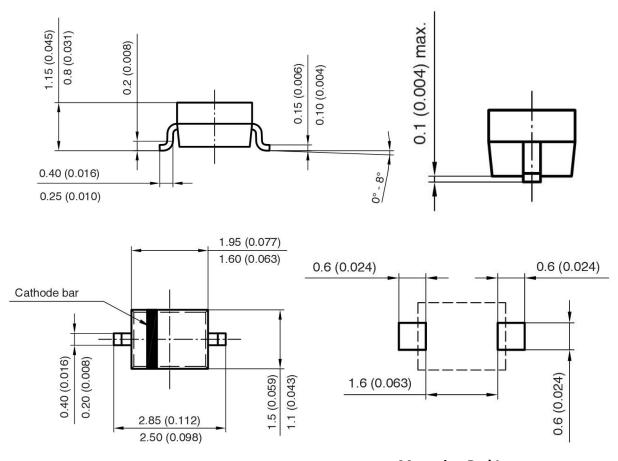
Characteristics Curves







ACKAGE OUTLINE DIMENSIONS in millimeters (inches) :SOD323



Mounting Pad Layout

Disclaimer

Specifications are subject to change without notice.

The device characteristics and parameters in this data sheet can and do vary in different applications and actual device performance may vary over time.

Users should verify actual device performance in their specific applications.